AGENDA

Day 1:

Fundamental of VLSI chip design methodology

Concepts of ASIC full customs & semi custom methodologies Hands on session

Day 2:

RTL to GDSII using semi custom design flow.

Concepts of analog digital mixed design and verification.

Hands on session.

REGISTRATION:

Details: umabala.ece@kanchiuniv.ac.in

Spot Registration is available Registration Fee-Rs.500/-

For Account Transfer:

APOGEE'08/AECE/EMA

A/C NO:510334488

Bank name: INDIAN BANK

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IFSC code no: IDIB000S145

CONTACT:

Mrs.V.Uma Mr.J.Vinoth Kumar -9790142915

Cheif Patrons

Prof.Dr.S.Jayarama Reddy, Chancellor, SCSVMV Prof.Dr.V.S.Vishnu Potty, Vice-Chancellor, SCSVMV

Patrons

Prof.Dr.G.Srinivasu,Registrar,SCSVMV Prof.Dr.G.Sriram, Dean (E&T), SCSVMV

Convener

Prof.V.Swaminathan HOD/ECE

Organizing Secretary Dr.G. Senthil Kumar

Associate Professor/ECE

Coordinators

Mrs.V.Uma

Assistant professor/ECE

Mr.J.Vinothkumar

Assistant Professor/ECE

Resource Persons

Experts from coreEL technologies, Bangalore.







SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA

(SCSVMV) (Deemed to be University U/S of UGC Act 1956) (Accredited with "A" Grade by NAAC) Enathur, Kanchipuram – 631561, www.kanchiuniv.ac.in



"IC DESIGN FLOW & VLSI TESTING USING MENTOR GRAPHICS EDA TOOLS"

ON 26 & 27TH MARCH 2018

ORGANIZED BY

DEPARTMENT OF ELECTRONICS MMUNICATYION & ENGINEERIN

IN ASSOCIATION WITH





ABOUT SCSVMV

The Maha Swamiji of Sri Kanchi
Kamakoti Peetam had cherished a desire to
revive kanchipuram to its pristine glory as a
center of learning. The Viswa
mahavidyalaya at Kanchi was established
with the benign Blessings of their
Holiness Pujyasri Jayendra Saraswathi
Swamiji and Pujyasri Sankara Vijayendra
saraswathi swamiji.

Sri Chandrasekharendra saraswathi Viswa Mahavidyalaya, was formed under the aegis of Sri Kanchi Kamakoti Peetam Charitable Trust in 1993, located in a sprawling complex of 50 acres at Enathur, at a distance of about 4 Kilometres from Kanchipuram.

ECE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1999. The Department offers Regular & part time B.E., ME., PhD.At present 800+ students are pursing above courses. The department has Laboratories viz., Computer Aided Design Lab, Micro processor & Embedded System Lab, Microwave Lab, Optical communication lab, Communication system lab and Electronics devices lab.

ABOUT COREEL UNIVERSITY PROGRAM

Department Of Electronics Engineering Was Started In 1986-87. The Department Has A Blend Of Experience And Well Qualified Faculty.

All The Laboratories Of The Department Are Well Equipped With Modern Instruments, Equipment And Electronic Design Automation (EDA) Tools.Department Has An Active IEEE Circuits And Systems Society Students Branch Chapter.The Department has Produced Several Notable Alumni Who Have Contributing Immensely To The Industry And Society.

ABOUT COREEL TECHNOLOGIES

Coreel University Program Focuses On
Bringing High End Electronics Design and
Embedded Computing Platforms To Labs Of
Colleges and University Enhancing the
Experience Of Technical Education And Real
Time Practice On World Beating
Technologies. The Induction Of Latest technology
Platforms Offers an Immersive Learning
Experience to Students and Trainers Ensures
That Forthcoming Industry Needs are Met
With. Coreel Enables The Academia To Push The
Barrier Of Industrial Learning Thanks To The
Substantive Support Of Its Technology Partners
And Solution Providers.

APPLICATION FORM

Name:
Designation:
Educational Qualification:
Name of the Institute:
Address
for Communication:
Email:
Mobile:
Professional experience:
Teaching:
Industry:
Accommodation needed:yes/no
Declaration:
The information furnished above is true
to the best of my knowledge.
Date:
Place:

Signature of the applicant